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[Fast Data Breakpoints - David Keppel \(1993\) \(Correct\) \(2 citations\)](#)

with a branch to a new handler. This recursive **application of breakpoints** builds a displaced handler [Kes90, Wah92] Each load and store instruction is **patched** with a jump to code that performs a test. The Fast Data **Breakpoints** David Keppel 3 May 1990, revised 14 April  
ftp.cs.washington.edu/tr/1993/04/UW-CSE-93-04-06.PS.Z

[CFL3D User's Manual \(Version 5.0\) - Sherrie Krist Bananepos \(1998\) \(Correct\) \(1 citation\)](#)

analysis. Its use for internal turbomachinery **applications** has been only as a basic research code thus  
. 48 LT41 -Number of **Dynamic Patched-Grid** Interfaces .49 LT42 -  
. 12 Utilizing **Patched** and/or Overlapped Grids .  
techreports.larc.nasa.gov/ltrs/PDF/1998/tm/NASA-98-tm208444.pdf

[Implementation And Evaluation Of Data Breakpoint Schemes In An.. - Roberts \(1996\) \(Correct\)](#)

In short, watchpointing could be a nice **application** of a **dynamic code patcher**, but building a  
88100/88200 [2]Part of this work included a **dynamic** code generator to do code **patching** for  
: 6 2.1.1 **Trap Patching** :  
manco.s.cs.utah.edu/papers/perobert\_thesis.ps.gz

[Causal Distributed Breakpoints - Fowler, Zwaenepoel \(1990\) \(Correct\) \(44 citations\)](#)

parallel program, relative to a node in a parallel **dynamic** program dependency graph [3]Their before state  
**Causal Distributed Breakpoints** Jerry Fowler Willy Zwaenepoel Department of  
www.cs.rice.edu/~willy/papers/icdcs90a.ps.gz

[Dynamic Patches for Live Musical Performance - Martin Kaltenb Runner \(Correct\)](#)

[17] or the Audiopad [18] are indeed musical **applications**. However, in the reacTable\*three additional  
**Dynamic Patches** for Live Musical Performance Martin  
**Dynamic Patches** for Live Musical Performance Martin Kaltenb  
www.lua.upf.es/mtg/publications/NIME04-Kaltenbrunner.pdf

[Analysis of Hardware and Software Approaches to Embedded.. - Chen, Kao, Huang \(Correct\)](#)

is switched from FGDM to BGDM, the user **application** program will resume its normal execution as if  
FGDM. If such allocation can be reconfigured or **dynamically** adjusted, then the number can be adjusted  
of approaches: software monitor and instruction **patching**. The software monitor uses software to check the  
www.jpriit.flinders.edu.au/confpapers/CRPITV6Chen.pdf

[Optimizing Patching Performance - Cai, Hua, Vu \(1999\) \(Correct\) \(42 citations\)](#)

technology for many important multimedia **applications**, such as home entertainment, digital video  
Unlike conventional multicast, **patching** is a **dynamic** multicast scheme which enables a new request to  
**Optimizing Patching Performance** Ying Cai Kien A. Hua Khanh Vu  
www.dsg.cs.ucf.edu/papers/spie99-cai.ps

[Breakpoints and Breakpoint Detection in Source Level.. - Koch, Kebschull, Rosenstiel \(1996\) \(Correct\) \(2 citations\)](#)

algorithmic level at least for some kinds of **applications** While there is much effort spent in methods  
we want to set a new **breakpoint**. All this is done **dynamically** in the implemented circuit. The detection of  
**Breakpoints and Breakpoint Detection in Source Level**  
www.fzi.de/sim/publications/1996004-paper.pdf

[Application Sharing - Architecture and Performance.. - Schoettner, Kassler.. \(Correct\)](#)

**Application Sharing -Architecture and Performance**  
(no compression of bitmaps) Interceptor: user32.dll (window-management) and gdi32.dll  
www-vs.informatik.uni-ulm.de/Papers/ACTS97/ACTS97.ps

[An Efficient Bandwidth-Sharing Technique for True Video on.. - Cai, Hua \(1999\) \(Correct\) \(2 citations\)](#)

6, 7]They can be grouped into two categories: **Dynamic Multicast**: In this approach [3, 4, 5, 6, 8, 9]  
S. A. E-mail: fcai, kienhuag@cs.ucf.edu Abstract **Patching** is a cost ecient channel-sharing technique for

application dynamic patching dll breakpoint setting - ResearchIndex document query

playback rate of the video. To find the optimal **settings** of the regular window and transition window, we  
[www.risg.cs.ucf.edu/papers/cai-acm-mm99.ps](http://www.risg.cs.ucf.edu/papers/cai-acm-mm99.ps)

A New Scheduling Scheme for Multicast True VoD Service - Ma, Shin (2001) (Correct)  
 multicast VoD system. Moreover, by using a novel **dynamic** merging algorithm, BEP significantly improves  
 we propose a new scheme, called the Best-Effort **Patching** (BEP) that offers a TVoD service in terms of  
 the **patching** window [4] Two simple approaches to **setting** the **patching** window are discussed in [8] greedy  
[kabru.eecs.umich.edu/papers/publications/2001/ma\\_pcm01.ps](http://kabru.eecs.umich.edu/papers/publications/2001/ma_pcm01.ps)

Evicted Variables and the Interaction of Global Register... - Ali-Reza Adl-Tabatabai (Correct)  
 at a **breakpoint**, nor do they consider the **dynamic** behavior of programs. Note that these metrics  
 to display the values of program variables at a **breakpoint**. However, problems arise if the program is  
 [8] The debugger supports the base operations of **setting** control **breakpoints**, inspecting data, and  
[www.cs.cmu.edu/ais/cmu.edu/project/iwarp/archive/ix-papers/popl93.ps](http://www.cs.cmu.edu/ais/cmu.edu/project/iwarp/archive/ix-papers/popl93.ps)

Integrating and Reusing GUI-Driven Applications - Grechanik, Batory, Perry (Correct)  
 Integrating and Reusing GUI-Driven **Applications** Mark Grechanik, Don Batory, and Dewayne E.  
 is emulation [15] Each COM component (or **dynamically** linked library (DLL) has a GUID (Globally  
 we outline a general technique, called code **patching** [10][18][19][4] for introducing an agent into  
[ftp.cs.utexas.edu/pub/predator/iiDE.pdf](http://ftp.cs.utexas.edu/pub/predator/iiDE.pdf)

Analysis of Random Jitter in a Clock Multiplying DLL... - Beek, Klumperink.. (2001) (Correct)  
 frequency as high as needed on-chip. Another **application** of clock multiplication lies in the fact that  
 Analysis of Random Jitter in a Clock Multiplying DLL Architecture R.C.H. van de Beek E.A.M.  
 area restrictions. Other practical issues such as **settling** behavior may also limit the minimum value of  
[www.stw.nl/programmas/profisc/proc2000/./proc-2001/beek.pdf](http://www.stw.nl/programmas/profisc/proc2000/./proc-2001/beek.pdf)

Debugging in Standard ML of New Jersey - Att Bell (Correct)  
 at all interesting events, including function **applications**, identifier bindings, the tops of function  
 more flexible **breakpoint setting** improved **dynamic** type reconstruction and numerous internal  
 for programs using signals more flexible **breakpoint setting** improved **dynamic** type reconstruction  
[www.li.uni.wroc.pl/~tomasz/sml/doc/tools/debug.ps.gz](http://www.li.uni.wroc.pl/~tomasz/sml/doc/tools/debug.ps.gz)

Customizing Mobile Applications - Schilit, Theimer, Welch (1993) (Correct) (26 citations)  
 Customizing Mobile **Applications** Bill N. Schilit Computer Science  
 Corporation [welch@parc.xerox.com](mailto:welch@parc.xerox.com) Abstract The **dynamics** of mobile systems require **applications** to  
[ftp.parc.xerox.com/pub/schilit/usmlc-93-schilit.ps.Z](http://ftp.parc.xerox.com/pub/schilit/usmlc-93-schilit.ps.Z)

Component Configurer: A Design Pattern for Component-Based... - Rosa, Silva (1997) (Correct)  
 2 Motivation How can a component-based **application** design take into account the problem of  
 components connection, aiming at supporting ad-hoc **dynamic** reconfiguration and the migration of components  
[albertina.inesc.pt/~ars/ps/europlop97-1.ps](http://albertina.inesc.pt/~ars/ps/europlop97-1.ps)

Optimal Patching Schemes for Efficient Multimedia Streaming - Sen, Gao, Rexford, Towsley (1999) (Correct)  
 (23 citations)  
 99 \Gamma 22 Abstract Multimedia streaming **applications** consume a significant amount of server and  
 removes any violations of the constraint K .fl **Dynamic** join/leave from multicast groups: If the  
 Optimal **Patching** Schemes for Efficient Multimedia Streaming  
[gaia.cs.umass.edu/pub/sen/Sen\\_Patching\\_TR99-22.ps.gz](http://gaia.cs.umass.edu/pub/sen/Sen_Patching_TR99-22.ps.gz)

A Dual-Loop Delay-Locked Loop Using Multiple... - Jung, Lee, Shim.. (2001) (Correct)  
 by skews and jitters of clock signals. In **applications** where the frequency multiplication is not  
 approach has the disadvantage of the increased **dynamic** noise sensitivity and jitter. In the  
 paper describes a dual-loop delay-locked loop (DLL) which overcomes the problem of a limited delay  
[www.iclab.snu.ac.kr/papers/jssc0105yjjung.pdf](http://www.iclab.snu.ac.kr/papers/jssc0105yjjung.pdf)

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## 1 1990: Fast breakpoints: design and implementation

Peter B. Kessler

April 2004

ACM SIGPLAN Notices, Volume 39 Issue 4

Full text available: pdf(1.64 MB)

Additional Information: full citation, abstract, re

In re-implementing fast breakpoints for stock hardware, I discovered the joys use. By "fast breakpoints" I mean inserting transfers of control to change the claim to have invented fast breakpoints: I have traced their use back to 1951 to novel uses. All I did was rediscover how easy it is to take statically compile the semantics of programs ...

## 2 GMSS graphic modelling and simulation system

R. R. Willis, W. P. Austell

March 1983

Proceedings of the 16th annual symposium on Simulation

Full text available: pdf(1.40 MB)

Additional Information: full citation, abstract, references, ci

GMSS is a simulation modelling system providing a tool kit of functions to support analysis. The goal of GMSS is to put simulation modelling into the hands of the

### 3 Software engineering: applications, practices tools (SE): A portable virtual machine for directing

Camil Demetrescu, Irene Finocchi

March 2004

Proceedings of the 2004 ACM symposium on Applied computing

Full text available:  pdf(206.36 KB)

Additional Information: full citation, abstract, references

Directors are reactive systems that monitor the run-time environment and react to events. Examples of directors are debuggers and tools for program analysis and software testing. We describe a cross-platform virtual machine that provides advanced facilities for

Keywords: debugging, directors, reversible computing, virtual machines

### 4 Visualization: An extensible framework for providing dynamic data structure visualizations

T. Dean Hendrix, James H. Cross, Larry A. Barowski

March 2004

Proceedings of the 35th SIGCSE technical symposium on Computer science education

Full text available:  pdf(537.96 KB)

Additional Information: full citation, abstract, references

A framework for producing dynamic data structure visualizations within the context of a Java IDE. Multiple synchronized visualizations of a data structure can be created with multiple external viewer models. The framework supplies a customizable viewer template drawing library and the Java Debugger Interface. Initial classroom use has demonstrated as well as its potential to be used as an aid ...

Keywords: algorithm animation, data structures, program visualization

### 5 System papers: data and software visualization and testing: MVT: a system for visualizing matrix-vector multiplication

Jan Lönnberg, Ari Korhonen, Lauri Malmi

May 2004

Proceedings of the working conference on Advanced visual interfaces

Full text available:  pdf(253.20 KB)

Additional Information: full citation, abstract, references

Software development is prone to time-consuming and expensive errors. Finding bugs (*debugging*) is usually done by executing the program with different inputs and checking the final results (*testing*). The tools that are currently available for debugging (*debuggers*) provide several potentially useful visualisation and interaction techniques. This article describes a new system (*MVT--Matrix Visualizer and Tester*) for visualizing matrix-vector multiplication.

Keywords: algorithm simulation, bytecode instrumentation, execution history, matrix-vector multiplication

### 6 A 100% portable inline-debugger

Jurgen Heymann

September 1993

ACM SIGPLAN Notices, Volume 28 Issue 9

Full text available:  pdf(752.49 KB) Additional Information: full citation, index terms

**7 Platforms: TOSSIM: accurate and scalable simulation of entire tinyOS appl**

Philip Levis, Nelson Lee, Matt Welsh, David Culler

November 2003 Proceedings of the first international conference on Embedded n

Full text available:  pdf(429.79 KB)

Additional Information: full citation, abstract, referenc

Accurate and scalable simulation has historically been a key enabling factor for TOSSIM, a simulator for TinyOS wireless sensor networks. By exploiting the system design, TOSSIM can capture network behavior at a high fidelity while scaling to probabilistic bit error model for the network, TOSSIM remains simple and efficient for a wide range of network interactions. Using TOSSIM, ...


Keywords: TOSSIM, sensor networks, tinyOS

**8 Teaching and learning computer programming: a survey of student problem instructional tools**

Miguel Ulloa

July 1980

ACM SIGCSE Bulletin, Volume 12 Issue 2

Full text available:  pdf(1.16 MB)

Additional Information: full citation, abstract, re

To improve introductory computer science courses and to update the teaching methods emphasizing structured programming and top-down design, automated instructional tools have been developed. The purpose of this paper is (1) to present, with the aid of beginning programmers experience difficulties; (3) to p ...

**9 A dynamic very high-level debugger for low-level microprograms**

N. J. Wahl, S. R. Schach, R. I. Winner

December 1986 ACM SIGMICRO Newsletter , Proceedings of the 19th annual workshop  
Issue 4

Full text available:  pdf(806.56 KB)


Additional Information: full citation, abstract, referen

Microcode debuggers may be classified according to the following three criteria: debugging, and the type of debugging facilities provided. A very high level interactive dynamic debugging of microcode written for the Perkin-Elmer 3220 is described. The user can debug microcode at a very high level because the microarchitecture of the format conceived by the ...

## 10 Practical data breakpoints: design and implementation

Robert Wahbe, Steven Lucco, Susan L. Graham

June 1993 ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1993 conference and implementation, Volume 28 Issue 6

Full text available:  pdf(1.37 MB)

Additional Information: full citation, abstract, references, ci

A data breakpoint associates debugging actions with programmer-specified code in an executing program. Data breakpoints provide a means for discovering program errors that cannot be isolated using control breakpoints alone. In practice, programmers rarely use data breakpoints because they are not implemented or prohibitively slow in available debugging software. In this paper, we describe the implementation of a practical data breakpoint facility in the

## 11 Dynamic currency determination in optimized programs

D. M. Dhamdhere, K. V. Sankaranarayanan

November 1998 ACM Transactions on Programming Languages and Systems (TOPLAS)

Full text available:  pdf(302.86 KB)

Additional Information: full citation, abstract, references, ci

Compiler optimizations pose many problems to source-level debugging of an optimized program. One such problem is to determine whether the value of a variable at a breakpoint—its actual value—is the same as its expected value. We propose the use of a minimal currency of a variable in source-level debugging and propose the use of a minimal currency of a variable in source-level debugging and propose the use of a minimal

Keywords: code instrumentation, code optimization, compiler, debugging optimization, dynamic slicing, minimal unrolled graph, source-level debugging

## 12 Extending the message flow debugger for MQSI

Shuxia Tan, Eshrat Arjomandi, Richard Paige, Evan Mamas, Simon Moser, Bill O'Neil

November 2001 Proceedings of the 2001 conference of the Centre for Advanced Studies

Full text available:  pdf(312.67 KB)

Additional Information: full citation, abstract, references, ci

Integration and management of applications play a key role in today's computing environment. The Message Queue Series Integrator (MQSI) is a component of MQSeries providing support for message flow. The key technology in MQSI is the notion of a message flow. Operations on a message, performed by a series of message processing

## 13 Session 24: software tools: A portable debugger for parallel and distributed

Doreen Cheng, Robert Hood

November 1994 Proceedings of the 1994 ACM/IEEE conference on Supercomputing


Full text available:  pdf(996.90 KB)

Additional Information: full citation, abstract, references, ci

We describe the design and implementation of a portable debugger for parallel and distributed systems. It incorporates a client-server model in order to isolate non-portable debugger components. The definition of a protocol for client-server interaction facilitates a high degree of portability. The implementation of a debugger for distributed computing environments is achieved

#### 14 Experiences with building distributed debuggers

Michael S. Meier, Kevan L. Miller, Donald P. Pazel, Josyula R. Rao, James R. Rus  
January 1996 Proceedings of the SIGMETRICS symposium on Parallel and distribut

Full text available:  pdf(1.34 MB)

Additional Information: full citation, references, index terms

#### 15 Tools: An explanation-based, visual debugger for one-way constraints

Bradley T. Vander Zanden, David Baker, Jing Jin  
October 2004 Proceedings of the 17th annual ACM symposium on User interface

Full text available:  pdf(696.45 KB)

Additional Information: full citation, abstract, referen

This paper describes a domain-specific debugger for one-way c  
makes use of several new techniques. First, the debugger displ  
dataflow graph, called a *constraint slice*, that is directly  
This technique helps the debugger scale to a system containing  
Second, the debugger presents a visual representation of the s  
color encodings to high ...

Keywords: constraint satisfaction, data structures, one-way cor  
visual debugging

#### 16 A thread-aware debugger with an open interface

Daniel Schulz, Frank Mueller

August 2000 ACM SIGSOFT Software Engineering Notes , Proceedings of the 2000  
on Software testing and analysis, Volume 25 Issue 5

Full text available:  pdf(347.13 KB)

Additional Information: full citation, abstract, references,

While threads have become an accepted and standardized model for expressir  
parallelism for the shared-memory model, debugging threads is still poorly su  
in debugging threads and offers solutions to them. The contributions of this p  
interface for debugging as an extension to thread implementations is propose  
debugging are identified and implemented wit ...

Keywords: active debugging, concurrency, debugging, open interface, threads

## 17 iWatcher: Efficient Architectural Support for Software Debugging

March 2004 ACM SIGARCH Computer Architecture News , Proceedings of the 31st  
Computer architecture, Volume 32 Issue 2



Full text available:  pdf(314.11 KB)

Additional Information: full citation

Recent impressive performance improvements in computer architecture have r  
debugging. Software debugging often relies on inserting run-time software che  
to find the root cause of a bug. Moreover, program execution typically slows d  
times. To address this problem, this paper introduces the Intelligent Watcher (i  
monitor dynamic execution with minimal overh ...

## 18 A Tool for Writing and Debugging Algebraic Specifications

May 2004 Proceedings of the 26th International Conference on Software Engin

Full text available:  pdf(207.73 KB)  Publisher Site

Additional Informa

Despite their benefits, programmers rarely use formal specifications, because t  
they require an up front investment in time. To address these issues, we prese  
and debug algebraic specifications. Given an algebraic specification, our tool in  
just like any regular Java class. The tool can also modify an existing applicatio  
interpreter instead of a hand-coded i ...

## 19 KDB: a multi-threaded debugger for multi-threaded applications

Peter A. Buhr, Martin Karsten, Jun Shih

January 1996 Proceedings of the SIGMETRICS symposium on Parallel and distribut

Full text available:  pdf(991.10 KB)

Additional Information: full citation, references, citings, index terms

## 20 Recompilation for debugging support in a JIT-compiler

Mustafa M. Tikir, Jeffrey K. Hollingsworth, Guei-Yuan Lueh

November 2002 ACM SIGSOFT Software Engineering Notes , Proceedings of the 20  
Program analysis for software tools and engineering, Volume 28 1

Full text available:  pdf(89.55 KB)

Additional Information: full citation, abstract, reference

A static Java compiler converts Java source code into a verifiably secure and c  
intermediate format, called Java *byte codes*. The Java byte codes can be eithe  
or translated into native code by Java Just-In-Time compilers. Static Java cor  
Java class files to be used by the source level debuggers. However, the debug  
architecture independent byte codes and most o ...

Keywords: Java, Java virtual machine debugger interface, debug information,  
watch, just-in-time compilation

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500 documents found. **Order: relevance to query.**

[A short proof of Dirac's theorem on the number of edges.. - Deuber, Kostochka, Sachs \(1996\)](#) (Correct)

[www.mathematik.uni-bielefeld.de/sfb343/preprints/pr96067.ps.gz](http://www.mathematik.uni-bielefeld.de/sfb343/preprints/pr96067.ps.gz)

[A generalized collision mechanism for stochastic particle.. - Rjasanow, Wagner](#) (Correct)

Perception of numerical methods in rarefied gas **dynamics**. Progr. Astronaut. Aeronaut.118, 211-226,

[www.wias-berlin.de/WIAS\\_publ\\_preprints\\_nr157.PS](http://www.wias-berlin.de/WIAS_publ_preprints_nr157.PS)

[System Support for Software Fault Tolerance in Highly Available.. - Sullivan \(1992\)](#) (Correct) (5 citations)

B link tree :195 5.3.7 **Dynamic** Hashing for POSTGRES :

System Support for **Software** Fault Tolerance in Highly Available Database

by Mark Paul Sullivan System Support for **Software** Fault Tolerance in Highly Available Database

[wuarchive.wustl.edu/packages/postgres/papers/ERL-M93-05.ps.Z](http://wuarchive.wustl.edu/packages/postgres/papers/ERL-M93-05.ps.Z)

[The System Of Two Spinning Disks In The Torus.. - Wojtkowski \(1993\)](#) (Correct)

in a recent paper [B-G] proposed to study the **dynamics** of spinning disks. They introduced the

[mpej.unige.ch/mp\\_arc/c/94/94-88.ps.gz](http://mpej.unige.ch/mp_arc/c/94/94-88.ps.gz)

[Actuability of Underactuated Manipulators - Lee, Xu \(1994\)](#) (Correct)

the resulting underactuated system can make use of **dynamic** coupling within the system for position control.

[pecan.srv.cs.cmu.edu/afs/cs.cmu.edu/user/chrislee/www/cmu-ri-tr-94-13.ps.gz](http://pecan.srv.cs.cmu.edu/afs/cs.cmu.edu/user/chrislee/www/cmu-ri-tr-94-13.ps.gz)

[Uniform Reconstruction of Gaussian Processes - Müller-Gronbach, Ritter \(1995\)](#) (Correct) (1 citation)

[ftp.math.fu-berlin.de/pub/math/publ/pre/1995/pr-a-95-26.ps.Z](http://ftp.math.fu-berlin.de/pub/math/publ/pre/1995/pr-a-95-26.ps.Z)

[CFL3D User's Manual \(Version 5.0\) - Sherrie Krist Bananepos \(1998\)](#) (Correct) (1 citation)

. 48 LT41 -Number of **Dynamic** Patched-Grid Interfaces .49 LT42 -

. 12 Utilizing Patched and/or Overlapped Grids .

[techreports.larc.nasa.gov/ltrs/PDF/1998/tm/NASA-98-tm208444.pdf](http://techreports.larc.nasa.gov/ltrs/PDF/1998/tm/NASA-98-tm208444.pdf)

[An Analytical Evaluation of Static Coupling Measures for Domain.. - Poels \(1998\)](#) (Correct)

passing between instances of the classes (i.e.**dynamic** coupling) 1)**Dynamic** coupling is the type of directly measure the external quality of an OO **software** system, they might be useful as early indicators coupling properties. 1. INTRODUCTION Whereas **software** users are primarily interested in quality

[www.econ.kuleuven.ac.be/tew/academic/infosys/Members/Snoeck/ECOOP98-OOPM.ps](http://www.econ.kuleuven.ac.be/tew/academic/infosys/Members/Snoeck/ECOOP98-OOPM.ps)

[Low Latency Word Serial CORDIC - Villalba, Lang \(1997\)](#) (Correct)

in rotation mode and in vectoring mode are **incompatible**, which makes it hard to have a unified

[ftp.ac.uma.es/pub/reports/1997/UMA-DAC-97-05.ps.gz](http://ftp.ac.uma.es/pub/reports/1997/UMA-DAC-97-05.ps.gz)

[Optimized Software Synthesis for Digital Signal.. - Jürgen Teich.. \(1998\)](#) (Correct) (3 citations)

sorts using the EA .10 2.3.2 **Dynamic** Programming Post Optimization .

Optimized **Software** Synthesis for Digital Signal Processing

within an infinite loop to generate a **software** implementation. Each schedule loop thereby is

[ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz](http://ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz)

[Practical Estimates of the Errors Associated with the.. - Fulton, Namkung, Melvin \(1992\)](#) (Correct)

[techreports.larc.nasa.gov/pub/techreports/larc/92/conf-rpqnde-92-fulton.ps.Z](http://techreports.larc.nasa.gov/pub/techreports/larc/92/conf-rpqnde-92-fulton.ps.Z)

[Dynamic Patches for Live Musical Performance - Martin Kaltenb Runner](#) (Correct)

**Dynamic Patches** for Live Musical Performance Martin

**Dynamic Patches** for Live Musical Performance Martin Kaltenb

developing the basic reacTable\* concepts within a **software** prototype only, simulating the tangible user

[www.iua.upf.es/mtg/publications/NIME04-Kaltenbrunner.pdf](http://www.iua.upf.es/mtg/publications/NIME04-Kaltenbrunner.pdf)

[Webs of Archived Distributed Computations for Asynchronous.. - Mani Chandy \(1997\)](#) (Correct) (2 citations)

systems composed of autonomous opaque objects with **dynamic** interfaces distributed across the Internet. We of our approach is that the component tools, **software**, data, and even participants are distributed In this paper, we describe the design of a **software** technology that allows any component of a [www.cs.caltech.edu/~kiriya/projects/papers/Supercomputing/supercomputing.ps](http://www.cs.caltech.edu/~kiriya/projects/papers/Supercomputing/supercomputing.ps)

What Is the BEST Spectrum Estimate? - Wei (1997) (Correct)

really well, especially for signals with large **dynamic** range Key References ffl D. J. Thompson, [www.ece.utexas.edu/~sakarya/courses/ee381k/lectures/15\\_Multiple\\_Windows/lecture15/lecture15.ps](http://www.ece.utexas.edu/~sakarya/courses/ee381k/lectures/15_Multiple_Windows/lecture15/lecture15.ps)

Identification Of Unknown Parameters For Heat Conductivity.. - Botkin (1995) (Correct)

S. Stable solutions of inverse problems in the **dynamics** of controlled systems. Proc. of the Steklov [www.appl-math.tu-muenchen.de/~botkin/hof444.ps](http://www.appl-math.tu-muenchen.de/~botkin/hof444.ps)

A Partial Approach to the Problem of Deadlocks in.. - Tricas.. (1998) (Correct)

can run in an automatic way. This means that the **software** in charge of the monitoring and control of the Conclusions and future work 0.1 Introduction The **software** design for concurrent systems has to deal with [www.cps.unizar.es/~ftricas/GISIRR9705.ps.gz](http://www.cps.unizar.es/~ftricas/GISIRR9705.ps.gz)

General Determinantal Representation Of Pseudoinverses Of.. - Stanimirovic (1996) (Correct)

[rattler.cameron.edu/EMIS/journals/MV/9612/ves96101.ps.gz](http://rattler.cameron.edu/EMIS/journals/MV/9612/ves96101.ps.gz)

High Level, Multi-Agent Prototypes from a.. - Buhr, Amyot.. (1998) (Correct) (1 citation)

new and old features must be resolved, often **dynamically** (telephony feature interaction provides an allows **software** designers to form completely **incompatible** views of the system picture, complicating and Abstract This paper joins new concepts in **software** design with the BDI reference model for agents [www.usecasemaps.org/UseCaseMaps/pub/4paam98.ps](http://www.usecasemaps.org/UseCaseMaps/pub/4paam98.ps)

Optimizing Patching Performance - Cai, Hua, Vu (1999) (Correct) (42 citations)

Unlike conventional multicast, **patching** is a **dynamic** multicast scheme which enables a new request to Optimizing **Patching** Performance Ying Cai Kien A. Hua Khanh Vu [www.dsg.cs.ucf.edu/papers/spie99-cai.ps](http://www.dsg.cs.ucf.edu/papers/spie99-cai.ps)

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## 1 Some requirements for architectural support of software debugging

Mark Scott Johnson

March 1982 Proceedings of the first international symposium on Architectural support for operating systems, Volume 10 , 17 Issue 2 , 4

Full text available: pdf(710.87 KB)

Additional Information: full citation, abstract, references,

Architectural support of high-level, symbolic debugging is described at three levels: desired debugging functionality, the debugger implementor's view of architectural functionality, and the computer architect's view of architectural features or architectural requirements. References are made where possible to computing systems that have been written from the viewpoint of debugger implementor.

Keywords: Architectural debugging support, Breakpoints, Debugging, Debugging support, Profiles, Symbolic debugging, Traces


## 2 iWatcher: Efficient Architectural Support for Software Debugging

March 2004 ACM SIGARCH Computer Architecture News , Proceedings of the 31st Annual Computer Architecture Conference, Volume 32 Issue 2

Full text available: pdf(314.11 KB)


Additional Information: full citation

Recent impressive performance improvements in computer architecture have reduced the time to debug software. Software debugging often relies on inserting run-time software checks to find the root cause of a bug. Moreover, program execution typically slows down by several times. To address this problem, this paper introduces the Intelligent Watcher (iWatch), which monitors dynamic execution with minimal overhead.


- 3 **Chiron-1: a software architecture for user interface development, maintenance**  
Richard N. Taylor, Kari A. Nies, Gregory Alan Bolcer, Craig A. MacFarlane, Kenneth  
June 1995 ACM Transactions on Computer-Human Interaction (TOCHI), Volume 12, Number 6  
Full text available:  pdf(2.65 MB) Additional Information: full citation, abstract, references, citing

The Chiron-1 user interface system demonstrates key techniques that enable its user interface. These techniques include separating the control-flow aspect they are concurrent and may contain many threads. Chiron also separates window dialogue and abstract presentation decisions via mechanisms employing a client application code from user interface ...

Keywords: artists, client-server, concurrency, event-based integration, user interface



- 4 **Retrospective on high-level language computer architecture**  
David R. Ditzel, David A. Patterson  
May 1980 Proceedings of the 7th annual symposium on Computer Architecture  
Full text available:  pdf(722.89 KB) Additional Information: full citation, abstract, references, citing

High-level language computers (HLLC) have attracted interest in the architecture during the last 15 years; proposals have been made for machines directed toward such as ALGOL,1,2 APL,3,4,5 BASIC,6,7 COBOL,8,9 FORTRAN,10,11 LISP,12,13

- 5 **Compiler and tool support for debugging object protocols**  
Sergey Butkevich, Marco Renedo, Gerald Baumgartner, Michal Young  
November 2000 ACM SIGSOFT Software Engineering Notes , Proceedings of the 8th symposium on Foundations of software engineering: twenty-first conference  
Full text available:  pdf(1.06 MB) Additional Information: full citation, abstract, references, citing

We describe an extension to the Java programming language that supports type checking and dynamic debugging of object protocols, and enforces constraints on the order in which methods may be called. Our Java is a statically checkable subset embedded in richer descriptions that can be checked. A statically checkable subtype conformance relation is based on normal forms (finite-state) process types, and is also very compact ...

Keywords: debugging, protocols, sequencing constraints

- 6 **Architecture-based runtime software evolution**  
Peyman Oreizy, Nenad Medvidovic, Richard N. Taylor  
April 1998 Proceedings of the 20th international conference on Software engineering  
Full text available:  pdf(1.28 MB)  Publisher Site Additional Information: full citation, references, citations, indexing

## 7 Retrospective on high-level language computer architecture

David R. Ditzel, David A. Patterson

August 1998 25 years of the international symposia on Computer architecture (see

Full text available:  pdf(836.16 KB)

Additional Information: full citation, references, index terms

## 8 A software instruction counter

J. M. Mellor-Crummey, T. J. LeBlanc

April 1989 ACM SIGARCH Computer Architecture News , Proceedings of the third i  
support for programming languages and operating systems, Volume 1

Full text available:  pdf(997.70 KB)

Additional Information: full citation, abstract, references,

Although several recent papers have proposed architectural support for progr  
processors do not yet provide even basic facilities, such as an instruction coun  
been forced to invent software solutions. This paper describes our implementa  
for program debugging. We show that an instruction counter can be reasonabl  
less than 10% execution overhead. Ou ...

## 9 A survey of rollback-recovery protocols in message-passing systems

E. N. (Mootaz) Elnozahy, Lorenzo Alvisi, Yi-Min Wang, David B. Johnson

September 2002

ACM Computing Surveys (CSUR), Volume 34 Issue 3

Full text available:  pdf(549.68 KB)

Additional Information: full citation, abstract, references, citi


This survey covers rollback-recovery techniques that do not require special lai  
the survey we classify rollback-recovery protocols into *checkpoint-based* and *I*  
rely solely on checkpointing for system state restoration. Checkpointing can b  
communication-induced. *Log-based* protocols combine checkpointing with logc  
in tuples call ...

Keywords: message logging, rollback-recovery

## 10 The MIT Alewife machine: architecture and performance

Anant Agarwal, Ricardo Bianchini, David Chaiken, Kirk L. Johnson, David Kranz,  
Kenneth Mackenzie, Donald Yeung


May 1995 ACM SIGARCH Computer Architecture News , Proceedings of the 22nd i  
Computer architecture, Volume 23 Issue 2

Full text available:  pdf(1.49 MB)

Additional Information: full citation, abstract, references, ci

Alewife is a multiprocessor architecture that supports up to 512 processing no  
cost-effective mesh network at a constant cost per node. The MIT Alewife mac  
architecture, demonstrates that a parallel system can be both scalable and pri  
to achieve these goals: software-extended coherent shared memory provides  
integrated message passing allows compiler and operat ...


- 11 Software architecture: An integrated architecture for distributed applications**  
Michael A. Bauer, Neil Coburn, Doreen L. Erickson, Patrick J. Finnigan, James W  
October 1993 Proceedings of the 1993 conference of the Centre for Advanced Studies in  
engineering - Volume 1

Full text available:  pdf(1.54 MB)

Additional Information: full citation, abstract, references, ci

The CORDS project addresses all phases in the life cycle of distributed application management. Workers in each of these phases are faced with added difficulties lacking or too low-level. CORDS provides a set of higher-level tools and a set of tasks of these workers. The realization of such an environment requires the integration of distributed system components and the amalgamation ...

- 12 Using object-oriented typing to support architectural design in the C2 style**  
Nenad Medvidovic, Peyman Oreizy, Jason E. Robbins, Richard N. Taylor  
October 1996 ACM SIGSOFT Software Engineering Notes , Proceedings of the 4th International  
Foundations of software engineering, Volume 21 Issue 6

Full text available:  pdf(1.35 MB)

Additional Information: full citation, abstract, references, ci

Software architectures enable large-scale software development. Component integration aspects of large-scale development, must be planned for during software design. It supports reuse by structuring inter-component relationships and verifying them in an architecture definition language (ADL). In this paper, we identify the issues in applying OO type theory to the C2 architecture ...


- 13 Session 4B: Emerging architectures and domains: Why is distributed systems hard?**  
Huw Evans  
September 2001 Proceedings of the 4th International Workshop on Principles of Distributed Systems

Full text available:  pdf(512.77 KB)

Additional Information: full citation, abstract, references, ci

This position paper takes the view that modern programming languages, their compilers, and their run-time systems do not adequately support the programmer in their day-to-day task of evolving large programs. Evolving programs is the dominant cost on these kinds of system projects and should be supported in this task as they should be. This is argued by presenting what has gone well at code development time and at system run-time ...

- 14 Fast detection of communication patterns in distributed executions**  
Thomas Kunz, Michiel F. H. Seuren  
November 1997 Proceedings of the 1997 conference of the Centre for Advanced Studies in  
engineering - Volume 1

Full text available:  pdf(4.21 MB)

Additional Information: full citation, abstract, references, ci

Understanding distributed applications is a tedious and difficult task. Visualization tools are often used to obtain a better understanding of the execution of the application. Poet, an event tracer developed at the University of Waterloo. However, these tools do not provide the user with the desired overview of the application. In our experiments, we show occurrences of non-trivial communication patterns ...

### 15 The MIT Alewife machine: architecture and performance

Anant Agarwal, Ricardo Bianchini, David Chaiken, Kirk L. Johnson, David Kranz, D. Yeung

August 1998 25 years of the international symposia on Computer architecture


Full text available:  pdf(1.58 MB)

Additional Information: full citation, references, in

### 16 Enhancing software reliability with speculative threads

Jeffrey Oplinger, Monica S. Lam

October 2002 Proceedings of the 10th international conference on Architectural su  
operating systems, Volume 37 , 36 , 30 Issue 10 , 5 , 5

Full text available:  pdf(1.47 MB)


Additional Information: full citation, abstract, referer

This paper advocates the use of a monitor-and-recover programming paradigm and proposes an architectural design that allows software and hardware to coo efficient and easier to program. We propose that programmers write monitorin execution semantics. Our architecture speeds up the computation by executin in parallel with the main computation. For ...

### 17 Software-extended coherent shared memory: performance and cost

D. Chaiken, A. Agarwal

April 1994 ACM SIGARCH Computer Architecture News , Proceedings of the 21ST  
Computer architecture, Volume 22 Issue 2

Full text available:  pdf(1.27 MB)

Additional Information: full citation, abstract, references, ci

This paper evaluates the tradeoffs involved in the design of the software-exte multiprocessor architecture that implements coherent shared memory through software mechanisms. For each block of memory, Alewife implements betwee pointers in hardware and allows software to handle requests when the pointer a flexible coherence interface that facilitates protocol soft ...

### 18 Hardware support for program debuggers in a paged virtual memory

David Abramson, John Rosenberg

June 1983 ACM SIGARCH Computer Architecture News, Volume 11 Issue 2

Full text available:  pdf(1.04 MB)


Additional Information: full citation, references, citings

## 19 Architecture of the space shuttle primary avionics software system

Gene D. Carlow

September 1984

Communications of the ACM, Volume 27 Issue 9

Full text available:  pdf(1.26 MB)

Additional Information: full citation, abstract, citations, in

PASS, perhaps the most complex flight computer program ever developed, establishing a well-structured system architecture at the front end of the deve

Keywords: PASS, PASS space shuttle, avionics system, space shuttle

## 20 Software architecture based on communicating residential environments

Erik Sandewall, Claes Strömberg, Henrik Sörensen

March 1981

Proceedings of the 5th international conference on Software engi

Full text available:  pdf(864.50 KB)

Additional Information: full citation, abstract, reference




This paper describes an alternative approach to software architecture, where t  
between operating systems, programming languages and compilers, and so fo  
organized as a set of self-contained environments which are able to communi  
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[Runtime Application Patching for High Availability with Carrier ...](#)

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... SoftICE is running, or in your **version** of Windows ... 6. Forget about protecting using **runtime-limits** or 30 ... in some cases it can make **patching** virtually impossible ...

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[www.parasoft.com/jsp/products/manuals.jsp?product=Insure&manual=insure/manuals/v5/windows/Readme.html](#) - 43k - [Cached](#) - [Similar pages](#)

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... that allows for simple run time **patching** of processes ... determine a web servers platform and **version** even after ... combination of IDA-Pro and **runtime debugging** tools ...

[www.blackhat.com/html/bh-asia-02/bh-asia-02-speakers.html](#) - 76k - Nov 20, 2004 - [Cached](#) - [Similar pages](#)

[dbx and System Libraries: Why Can't dbx Read My Process or Core...](#)

... Problems might also occur when **patching** a system, and ... Library Path Name, Solaris Operating Environment **Version**. ... to Sun, he worked on **runtime** support libraries ...

[developers.sun.com/tools/cc/articles/DebugLibraries/DebugLibraries\\_content.html](#) - 30k - [Cached](#) - [Similar pages](#)

[Mac-On-Linux General: MOL 0.9.49 freezing at "Debugger Installed"](#)

... I'm using kernel **version** 2.2.17pre18. ... **runtime** patched Trying to apply MOL **runtime** patches ... Examining '/boot/System.map' \*\*\*\* Success \*\*\*\* **Patching** the kernel ...

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### Apple II Technical Notes Developer Technical Support Apple ...

... too—you don't have to check the ROM **version**. ... regains control is much more of a **compatibility** risk, because ... a general purpose utility like a **debugger**, it may ...

[www.gno.org/pub/apple2/doc/apple/technotes/ljgs/tn.ljgs.101](http://www.gno.org/pub/apple2/doc/apple/technotes/ljgs/tn.ljgs.101) - 7k - [Cached](#) - [Similar pages](#)

### Programming Tools Guide

... linker Checking for run-time **compatibility** Dynamic linking ... An sdb session adb: absolute **debugger** Starting adb ... tools lprof Creating a profiled **version** of a ...

[docsrv.sco.com:507/en/tools/CONTENTS.html](http://docsrv.sco.com:507/en/tools/CONTENTS.html) - 101k - [Cached](#) - [Similar pages](#)

### Jan 90 Mousehole

... OK, A couple of further updates on MacIIci **compatibility**. ... 4 I think) it gives you a "Bad MultiFinder **version**" error message ... What do folks do for a **debugger**? ...

[www.mactech.com/articles/mactech/Vol.06/06.01/Jan90Mousehole/](http://www.mactech.com/articles/mactech/Vol.06/06.01/Jan90Mousehole/) - 32k - [Cached](#) - [Similar pages](#)

### MEKA Homepage

... This is a very basic **debugger** and it doesn't ... Other changes in this **version** includes an improved **patching** ... the MEKA.NAM database and the **compatibility** list were ...

[www.smspower.org/meka/](http://www.smspower.org/meka/) - 23k - Nov 20, 2004 - [Cached](#) - [Similar pages](#)

### PalmSource highlights new operating system, developer tools

... is releasing this week a Palm OS 5 **Compatibility** CD, with a preliminary **version** of Palm ... compatability with OS 5, and the Palm Universal **Debugger**, which is ...

[www.nwfusion.com/news/2002/0205palmsource.html](http://www.nwfusion.com/news/2002/0205palmsource.html) - 48k - [Cached](#) - [Similar pages](#)

### Frequently Asked Questions

... Use the **debugger** or the Solaris system utility pstack to ... the current **version** of SmartHeap for VC 6 **compatibility**. ... want your 4.x or later **version** of SmartHeap ...

[www.microquill.com/kb/faq\\_ans.htm](http://www.microquill.com/kb/faq_ans.htm) - 101k - [Cached](#) - [Similar pages](#)

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### Release Notes for Ladebug Version 67

... script that calls ladebug -gui, supporting **compatibility** with old ... a problem with using any **version** of ladebug ... some machines, where using the **debugger** hangs or ...

[nf.apac.edu.au/facilities/software/LADEBUG/release-notes.html](http://nf.apac.edu.au/facilities/software/LADEBUG/release-notes.html) - 101k - [Cached](#) - [Similar pages](#)

### [PDF] TotalView

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... systems, TotalView supports debugging pthread programs running in pthread-**compatibility** mode or ... port TotalView and the TotalView **Debugger** Server ... **Version** 6.1.0-2 ...

[www.etnuc.com/Documentation/rel5/pdf/platforms\\_6.0.0-2.pdf](http://www.etnuc.com/Documentation/rel5/pdf/platforms_6.0.0-2.pdf) - [Similar pages](#)

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### VT Emulation Network - NES Emulators

... of mappers, save-state support, a **debugger** and other ... and may have little or no **compatibility** with commercial ... The DOS **version** of RockNES was the emulator that ...

[www.vtemulation.net/emulators/nas.php](http://www.vtemulation.net/emulators/nas.php) - 28k - [Cached](#) - [Similar pages](#)

### BSDi - ELF FAQ for BSD/OS 4.x, version 1.3

... Our compiler and **debugger** support two symbolic debugging ... shared libraries, we would expect **compatibility** rather than be ... If you define your own **version** of malloc ...

[www.pix.net/software/bosdos/elf\\_faq.html](http://www.pix.net/software/bosdos/elf_faq.html) - 90k - [Cached](#) - [Similar pages](#)